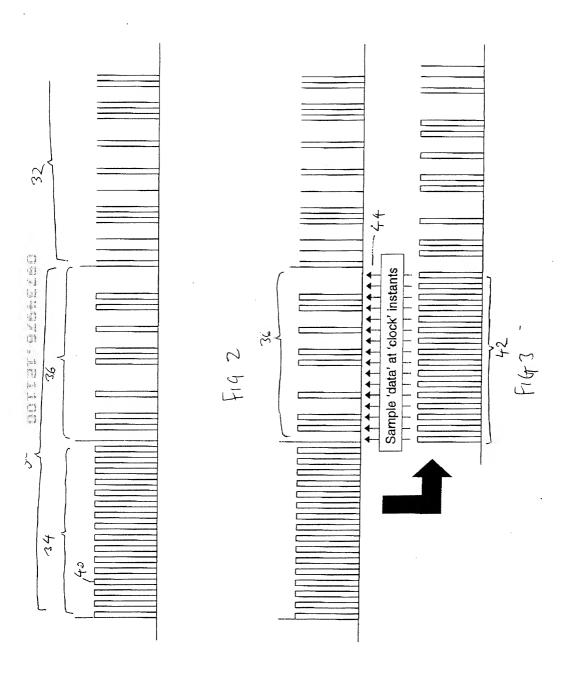
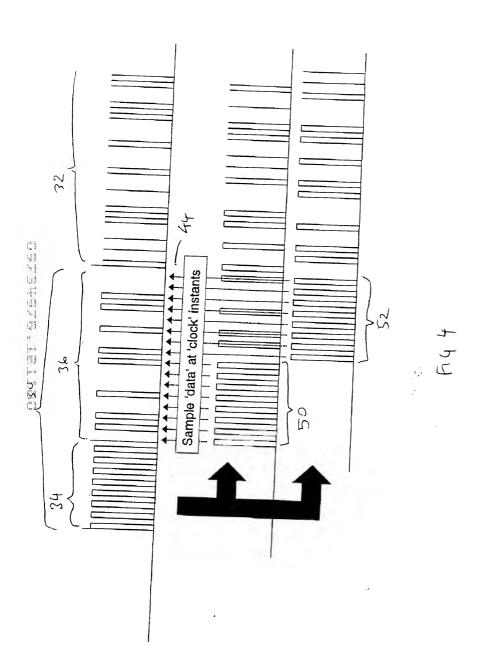
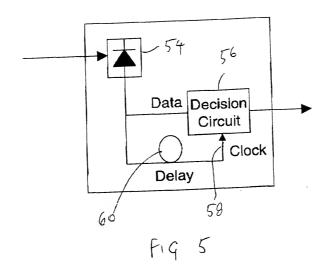


Fi G 1







Data1 Decision Circuit

Clock1 58a

Clock2 58b

Data2 Decision Circuit

F46